

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent No.: US 7,024,598 B2
Inventor: Jeong et al.
Issued: April 4, 2006

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE WITH A FAIL BIT
DETECTING SCHEME AND METHOD FOR COUNTING THE NUMBER OF
FAIL BITS

Ser. No.: 10/003,390 Filed: October 30, 2001
Group Art Unit: 2133 Examiner: Christine T. Tu

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

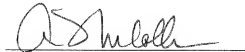
Upon comparison of the above-identified original Letters Patent with applicant's copy of the application therefore, the following error in the original Letters Patent has been noted:

At column 13, line 25, please replace "to registering" with --to register--

Applicant respectfully requests that a Certificate of Correction calling attention to the above error be issued pursuant to Rule 322 of the Patent Office and Title 35, United States Code, Section 254. Two copies of a Certificate of Correction form are enclosed.

Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.



Alan T. McCollom
Reg. No. 28,881

MARGER JOHNSON & McCOLLOM, P.C.
210 SW Morrison Street, Suite 400
Portland, OR 97204
503-222-3613